

Amendments to the Specification:

Please replace the specification paragraphs beginning at page 3, lines 3 to 15 with the following amended paragraph:

In accordance with a first aspect of the invention, a voltage-controlled oscillator circuit connected to supply and reference voltages for radio frequency operation is disclosed, the circuit comprising at least one inductor; at least one varactor connected in parallel with the at least one inductor; a pair of p-channel MOS transistors connected across the at least one varactor, each p-channel transistor having source, drain, and gate terminals, wherein the drain terminal of the first of the pair of p-channel MOS transistors is connected to the gate terminal of the second of the pair of p-channel MOS transistors and the drain terminal of second of the pair of MOS transistors being is connected to the gate terminal of the first of the pair of MOS transistors; and biasing means for providing a biasing current to the voltage-controlled oscillator circuit and for substantially widening the tuning range thereof, the biasing means being configured according to one of a biasing n-channel transistor connected to the supply voltage and a biasing self-biasing p-channel transistor connected to the reference voltage, wherein the biasing means is connected to the pair of p-channel MOS transistors and the at least one inductor.

Please delete the specification paragraphs beginning at page 3, lines 17 to 29.

Please replace the specification paragraphs beginning at page 3, line 31 to page 4, line 10 with the following amended paragraph:

In accordance with a third second aspect of the invention, a method for configuring a voltage-controlled oscillator circuit connected to supply and reference voltages for radio frequency operation, the method comprising the steps of providing at least one inductor; connecting at least one varactor in parallel with the at least one inductor; connecting a pair of p-channel MOS transistors across the at least one varactor, each p-channel MOS transistor having source, drain, and gate terminals, wherein the drain terminal of the first of the pair of p-channel MOS transistors is connected to the gate terminal of the second of the pair of p-channel MOS transistors and the drain terminal of second of the pair of p- channel MOS transistors being is connected to the gate

terminal of the first of the pair of p-channel MOS transistors; and providing biasing means for providing a biasing current to the voltage-controlled oscillator circuit and for substantially widening the tuning range thereof, the biasing means being configured according to a biasing n-channel transistor connected to the supply voltage and a biasing self-biasing p-channel transistor connected to the reference voltage, wherein the biasing means is connected to the pair of p-channel MOS transistors and the at least one inductor.

Please replace the specification paragraphs beginning at page 5, lines 11 to 18 with the following amended paragraph:

Embodiments of the invention are described with reference to Figs. 4a to 4d. The preferred embodiment is described with reference to Figs. 4a and 4b. In such an embodiment, an LC-tank VCO core is biased using two MOS transistors. In an alternate embodiment described with reference to Fig. 4c, an LC-tank VCO core is biased using a p-channel MOS transistor connected to a reference voltage such as ground. In a further alternative embodiment described with reference to Fig. 4d, an LC-tank VCO core is biased using an n-channel MOS transistor connected to a supply voltage such as VDD.

Applicant submits amendment to the specification, specifically the summary and detailed description, to impart clarity thereto and to improve descriptive consistency with the claims that have been amended therewith.

Applicant respectfully submits that no new matter is added.